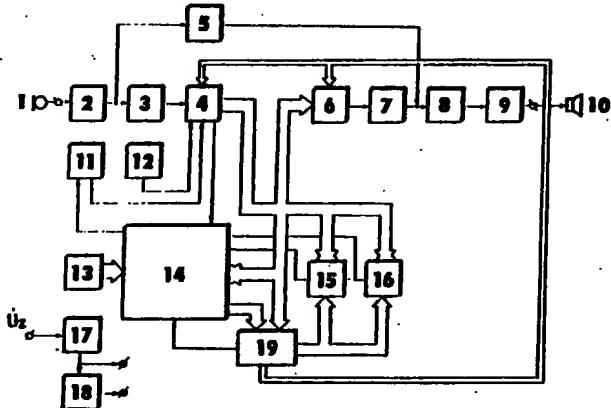




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(54) Title: METHOD AND ELECTRONIC SYSTEM OF THE DIGITAL CORRECTOR OF SPEECH FOR STUTTERING PEOPLE



(57) Abstract The invention is solving the subject of the design of the method of digital correction of speech and of the microprocessor-based active speech corrector for stuttering people. Type of digital correction of speech for stuttering people, through electronic signal processing in acoustical feedback loop is characteristic in this, that acoustical signal obtained from the stuttering person via microphone or contact microphone is divided into two frequency bands, from which higher bandwidth is passed in analog domain after amplification to the output electroacoustic transducer, and lower bandwidth is sampled and processed with analog-to-digital converter. Sampled signal obtained in this fashion is processed digitally via tone shifting downwards and simultaneously delayed with regulation of loudness. Processed in this fashion lower bandwidth of frequencies is next mixed with high-pass analog signal and as output signal is sent to electroacoustic transducer, for example earphones or bone vibrator. The invention includes also the electronic digital processor used for the implementation of the above algorithms. The processor configuration is presented in the figure. List of symbols used in the figure is as follows: (1) electroacoustic input transducer in particular microphone or contact microphone; (2) input amplifier; (3) low-pass filter; (4) analog-to-digital converter; (5) regulator of the output signals amplification level; (6) digital-to-analog convertor; (7) low-pass reconstruction filter; (8) gain regulator of the output signal; (9) output amplifier; (10) output electroacoustic transducer in particular earphone or bone vibrator; (11) sampling circuitry of the analog-to-digital converter; (12) buffer memory of the analog-to-digital converter; (13) block of multi-section programming sequence switch of the microprocessor; (14) microprocessor; (15) programmable memory of the microprocessor; (16) operating memory of the microprocessor; (17) voltage converter of the supply voltage; (18) voltage inverter of the supply voltage; (19) address decoder for the microprocessor; (Uz) supply voltage for the circuit.

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Method and Electronic System of the Digital Corrector of Speech
for Stuttering People

Description of the Invention

1. Technology domain

- 5 The basis of the invention is the method and electronic system of the Digital Corrector of Speech for stuttering people. The invention is concerned with the structure of the small electronic, digital systems for processing of the acoustical signals for the therapy of the speech disorders, in particular for the stuttering people, for the use in various everyday situations.
- 10 The method stems from psychoacoustics of hearing and speech production and the device design is based on the domain of digital electronics.

2. Present state of technology

- There are known numerous solutions for the speech correctors in which analog processing circuitry is used for processing of the acoustical signal.
- 15 It is known from the Russian patent description No. 66.680 the solution for the electronic echo-corrector of speech, which contains linked in series: microphone, input amplifier, system for echo generation, output amplifier and electroacoustic transducer in the form of the headphones. In the system, according to this invention especially structured subsystem for the 20 generation of the echo is used, which is build from twenty linked in series inductor-capacitor blocks with individual correcting amplifiers. This system accomplishes the delay of the speech signal at discrete values in the range from 0 to 120 ms. The necessity of usage in this system large quantity of inductor-capacitor-based filters and also passive intermediate elements 25 leads to non-uniform transfer characteristics at various values of delay coefficient. Device based on this Russian invention does not qualify for miniaturization and digitalization.

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It is also known and used commercially solution of the echo-reverberator speech correctors for stutterers according to the Polish patent description No. 130.362. This device contains known microphone linked with input of known
30 input amplifier, output of which is connected to input of known echo and reverberation circuit with output amplifier and speaker. Important in this invention is, that circuit for generation of echo and reverberation contains input low-pass filter, which is connected with analog-to-digital converter, which is connected with binary shift register, and it in turn is connected
35 with digital-to-analog converter, connected via output of low-pass filter with output of amplifier. In this circuit it is important, that control input of the binary shift register and clock input of the analog-to-digital converter are connected with clock generator with regulated frequency. The
40 input of the input amplifier and output of the output amplifier are connected with telephone antilocal splitting system, which allows for the connection with the local, public telephone network.

Described system of the echo-reverberator speech corrector, although fulfills well its assumptions and qualifies for the miniaturization necessary
45 for device carried by the user, exhibits fault, which is a small effectiveness for correcting speech disorders, such as deep breakage in the speech production or interference in the speech production caused by organic disorders of the hearing.

3. Revealing of the invention

50 The invention solves the subject of a new type of the speech correction method and a new type of the construction of the speech corrector, not showing the drawbacks of the known solution and allows for the correction of the speech fluency of the patient without masking of the acoustical signal of his speech and acoustical background signals, and it allows also for
55 successful audiological treatment of the numerous speech disorders without neurosurgical intervention. The device may be used in various situations in every day living by the stuttering person. Characteristic for the device is, that it could be used continuously by the stuttering person, who carries it.

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Method of digital correction of speech for the stuttering people, through
60 electronic signal processing in acoustical feedback loop, is characteristic
in this according to the invention, that acoustical signal obtained from the
stuttering person via microphone or contact microphone is splitted into two
frequency bands, from which bandwidth with higher frequencies (for example
65 higher than 4000 Hz) is passed in analog domain after amplification to the
output electroacoustic transducer, and bandwidth with frequencies lower then
for example 4000 Hz is sampled and processed with analog-to-digital converter
with regulated sampling frequency typically being not lower then 8 kHz and
with resolution not lower then 8 bit. Signal obtained in this fashion is
digitally via tone shifting downwards, particularly by 1 or 3/4 or 1/2 or 1/4
70 octave or upwards by 1/4 or 1/2 octave, and simultaneously delayed from 0 to
250 ms, particularly delay discreetly switchable for example in sixteen
steps. Processed in this fashion lower bandwidth of frequencies is next mixed
with high-pass analog signal and as an output signal is regulated as to the
amplitude and is sent to the electroacoustic transducer, for example
75 headphones or bone vibrator.

Optional modification of the method according to the invention is, that
from the lower frequency bandwidth one extracts through known digital
filtering algorithms the vocal tone of the speech signal, and transposes its
spectrum via shift in frequency or inversion of the phase or delay and next
80 use it for the resynthesis of the speech signal with changed vocal tone.

Another modification of the algorithm uses a procedure for the automatic
triggering the lower frequency band processing when the input signal
delivered by the transducer is sufficiently high in amplitude. Thus, the
signal processing procedures are executed only in the case of speech
85 production by the stutterer, otherwise the signal remains unaffected by any
of the processing procedures and is not transmitted by the signal processing
channel. The threshold of the digital signal processing channel triggering is
selected using the multiple switch built-in to the device.

Proposed digital system for correction of speech for the stuttering
90 people, containing known microphone and amplifier combined with electronic
system for signal processing connected to headphone or bone vibrator, is
characteristic by this in the invention, that electroacoustic transducer,

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particularly microphone or contact microphone is connected via linked in series input amplifier and low-pass filter to analog signal input of 95 analog-to-digital converter working in conjunction with sampling circuitry and buffer memory, and having control input connected to the microprocessor, equipped in input multi-section switch for programming sequences.

Thanks to application in the circuit according to the invention of the 100 microprocessor combined with the input analog-to-digital convertor and output digital-to-analog convertor, it is possible to process digital samples of the speech with the microprocessor. In particular, processing is performed on the lower part of the frequency band, limited for example to 4000 Hz, while the higher band above this frequency is passed without change. Because of the fact, that it is possible to choose various work programs of the device, and 105 also regulation of the volume by the patient, it allows very comfortable use of the device and adjustment of its parameters to actual needs of the patient. Programs for sound processing which are performed via microprocessor system include in particular: regulated frequency transposition (shift in frequency scale) upward or downward, regulated sound delay in the range from 110 0 to 250 ms, extraction of vocal tone and its resynthesis, phase inversion of the signal in chosen frequency bands, reduction of the level of self-hearing of the vocal tone via phase inversion and shift in time. The mentioned program functions may be selected through the use of the built-in multiple switch (dip switch).

115 4. Description of figure

The invention is described in detail in the example of its electronic implementation and is illustrated on the drawing introducing simplified block diagram for the digital speech corrector presented in Figure 1.

120 The illustrated on the drawing circuit of the digital speech corrector works as follows: basic function of shifting in the frequency band with simultaneous delay of the signal in the acoustical feedback path from microphone 1 to earphones 10 is accomplished in following manner, that signal from the input transducer 1 is relayed to input amplifier 2 and after amplification is filtered by low-pass filter 3. Non-filtered high-band

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125 portion of the acoustical input signal is relayed via regulator 5 to output amplifier 9 via level regulator 8, and then to output electroacoustic transducer 10. Signal in the frequency band lower than 4000 Hz is processed by the analog-to-digital converter 4 containing sampling circuitry 11 and individual buffer memory 12. Digital signal is read by the microprocessor 14
130 from the signal output of the analog-to-digital converter 4, and next it is processed according to the program chosen by the user with the multi-section switch (dip switch) 13. Used in the circuitry block of multi-section switch 13 is in practice eight-section switch of the DIP type, used during the operation of the device to choose one of many programming operations. State of
135 this switch is continuously read by the microprocessor 14. Signal is shifted up or down in the spectrum as it is described in the method. Samples of the signal are stored in the operating memory 16 of the RAM type. Buffering of the samples is important because of the realization of algorithms for signal processing. Set of programs for correction of various types of speech disorders is stored in the ROM-type memory (EPROM) 15. Applied in the
140 circuitry according to the invention address decoder 19 allows for decoding of the memory addresses and analog-to-digital converter addresses. After processing in built in such a way microprocessor system, samples of the acoustical signal in low frequency bandwidth are converted to analog signal via digital-to-analog converter 6. Obtained in this fashion signal with changed frequency spectrum and delayed in time goes to low-pass filter 7, where is smoothed (not needed components are removed). Output amplifier 9 after mixing signals together, amplifies obtained in such a way signal, as to be able to drive output electroacoustic transducer 10 (for example earphones). Source of the power is the rechargeable battery Uz, of which
145 voltage is converted to stable supply voltage in the circuit 17 and via voltage inversion in circuit 18 to the negative supply voltage.

In another implementation of the device it is possible to combine circuits 4, 11, 12, 14, 19, 15, 16, 6 in common signal processing block (circuit, chip).

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5. The best way to produce the invention

The electronic circuitry may be produced using the modern automated technology. Main elements of the device should be placed in one electronic chip using the ASIC chip production technique. The chip as well as the
160 remaining electronic elements should be prepared for the surface mounting and should be placed on the single small electronic printed board. The DIP switch (13) and the potentiometers (5) and (8) as in the fig. 1 and small microphone or small socket for contact transducer and small socket for the headphone should be mounted also to this board. The electronic board should be placed
165 in the small plastic casing together with miniature accumulator batteries. The casing have to be designed in such a way that the potentiometers, DIP switch and sockets are to be available to the user without opening the casing.

The device may be carried by the user in a shirt pocket or in the case of
170 further miniaturization may be placed behind his ear. The input and output transducers should be connected to the device using flexible thin cables with small plugs.

6. Industrial application

The device may be a subject to the electronic automated lot production.
175 The plastic casing production may be also automatic using the injection moulding machines. The process of the testing of the device based on the computer measurement arrangement and may be also performed automatically.

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Method and Electronic System of the Digital Corrector
of Speech for Stuttering People

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Patent Claims

1. Type of digital correction of speech for stuttering people, through electronic signal processing in acoustical feedback loop, characteristic in this according to the invention, that acoustical signal obtained from the stuttering person via known microphone or contact microphone is divided into two frequency bands, from which bandwidth with frequencies not lower than for example 4000 Hz is passed in analog domain after amplification to the output electroacoustic transducer, and signal in bandwidth with frequencies lower than for example 4000 Hz is triggered and then sampled and processed with analog-to-digital convertor, sampled and quantized signal obtained in this fashion is processed digitally via tone shifting downwards, particularly by 1 or 3/4 or 1/2 or 1/4 octave or upwards by 1/4 or 1/2 octave and simultaneously delayed from 0 to 250 ms, particularly delay discreetly switchable for example in sixteen steps, with simultaneous regulation of loudness and processed in this fashion bandwidth of frequencies lower than 4000 Hz is next mixed with high-pass analog signal and as output signal is sent to electroacoustic transducer, for example earphones or bone vibrator.
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195
2. Method according to claim 1 characteristic in this, that from bandwidth with frequencies lower than for example 4000 Hz one extracts through digital filtering algorithms vocal tone of the speech signal, and transposes its spectrum via shift in frequency or inversion of the phase or delay and next use it for resynthesis of the speech signal with changed vocal tone.
200
3. Circuit for the digital speech corrector for stuttering people including microphone and amplifier combined with signal processing electronic circuitry connected to earphone or bone-vibrator characteristic in this, that known input electroacoustic transducer, in particular microphone or contact microphone signal from the input transducer 1 is relayed to input amplifier 2 and after amplification is filtered by low-pass filter 3, when non-filtered high-band portion of the acoustical input signal is relayed via regulator 5 to output amplifier 9 via level regulator 8, and then to output
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210 electroacoustic transducer 10. Filtered by the low-pass filter 3 signal in
the frequency lower band is processed by the analog-to-digital converter 4
containing sampling circuitry 11 and individual buffer memory 12 and produced
in such a way digital signal is read by the microprocessor 14 from the signal
output of the analog-to-digital converter 4, and next it is processed
215 according to the program chosen by the user with the multi-section switch
(dip switch) 13 state of which is continuously read by the microprocessor 14.
Processed signal is shifted up or down in the spectrum as it is described in
the method. Samples of the signal are stored in the operating memory 16 of
220 the RAM type. Set of programs for correction of various types of speech
disorders is stored in the ROM-type memory (EPROM) 15. Applied in the
circuitry according to the invention address decoder 19 allows for decoding
of the memory addresses and analog-to-digital converter addresses. After
processing in built in such a way microprocessor system, samples of the
225 acoustical signal in lower frequency bandwidth are converted to analog
signal via digital-to-analog converter 6. Obtained in this fashion signal
with changed frequency spectrum and delayed in time goes to low-pass filter
7, where is smoothed (not needed components are removed). Output amplifier 9
after mixing signals together, amplifies obtained in such a way signal, as to
be able to drive output electroacoustic transducer 10 (for example
230 earphones). Source of the power is the rechargeable battery Uz, of which
voltage is converted to stable supply voltage in the circuit 17 and via
voltage inversion in circuit 18 to the negative supply voltage.

4. Circuitry according to Claim 3, characteristic in this, that
microprocessor is connected with two identical buses for digital-to-analog
235 conversion of the acoustical signal, appropriately to the left ear and to the
right ear, connected to one common input electroacoustic transducer, and
having individual output electroacoustic transducers.

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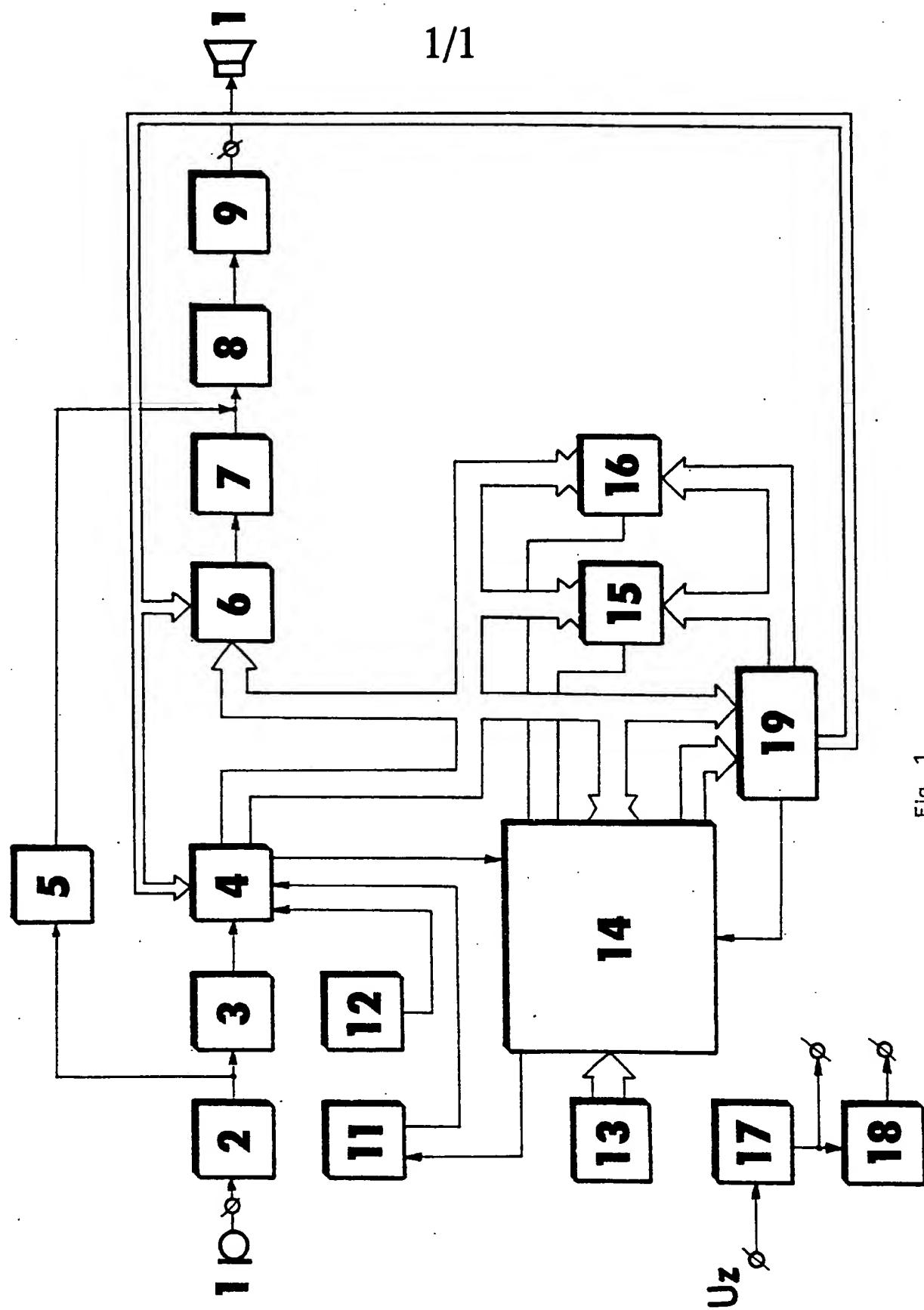


Fig. 1

INTERNATIONAL SEARCH REPORT

PCT/CA 93/00159

International Application No.

I. CLASSIFICATION OF SUBJECT MATTER (if several classification symbols apply, indicate all)⁶

According to International Patent Classification (IPC) or to both National Classification and IPC

Int.C1. 5 A61F5/58; G10L3/02

II. FIELDS SEARCHED

Minimum Documentation Searched⁷

Classification System	Classification Symbols
Int.C1. 5	A61F ; G10L

Documentation Searched other than Minimum Documentation
to the Extent that such Documents are Included in the Fields Searched⁸III. DOCUMENTS CONSIDERED TO BE RELEVANT⁹

Category ¹⁰	Citation of Document, ¹¹ with indication, where appropriate, of the relevant passages ¹²	Relevant to Claim No. ¹³
A	US,A,4 464 119 (VILDGRUBE) 7 August 1984 see column 2, line 23 - line 66 see claims 1,2 ----	1-4
A	US,A,4 051 331 (STRONG) 27 September 1977 see abstract see claims 1-6 -----	2

¹⁰ Special categories of cited documents :

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IV. CERTIFICATION

Date of the Actual Completion of the International Search

06 AUGUST 1993

Date of Mailing of this International Search Report

13 AUG 1993

International Searching Authority

EUROPEAN PATENT OFFICE

Signature of Authorized Officer

FARASSOPOULOS A.

**ANNEX TO THE INTERNATIONAL SEARCH REPORT
ON INTERNATIONAL PATENT APPLICATION NO.**

CA 9300159
SA 72879

This annex lists the patent family members relating to the patent documents cited in the above-mentioned international search report.
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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US-A-4464119	07-08-84	None	
US-A-4051331	27-09-77	None	